

FIG. 3 - Prior Art

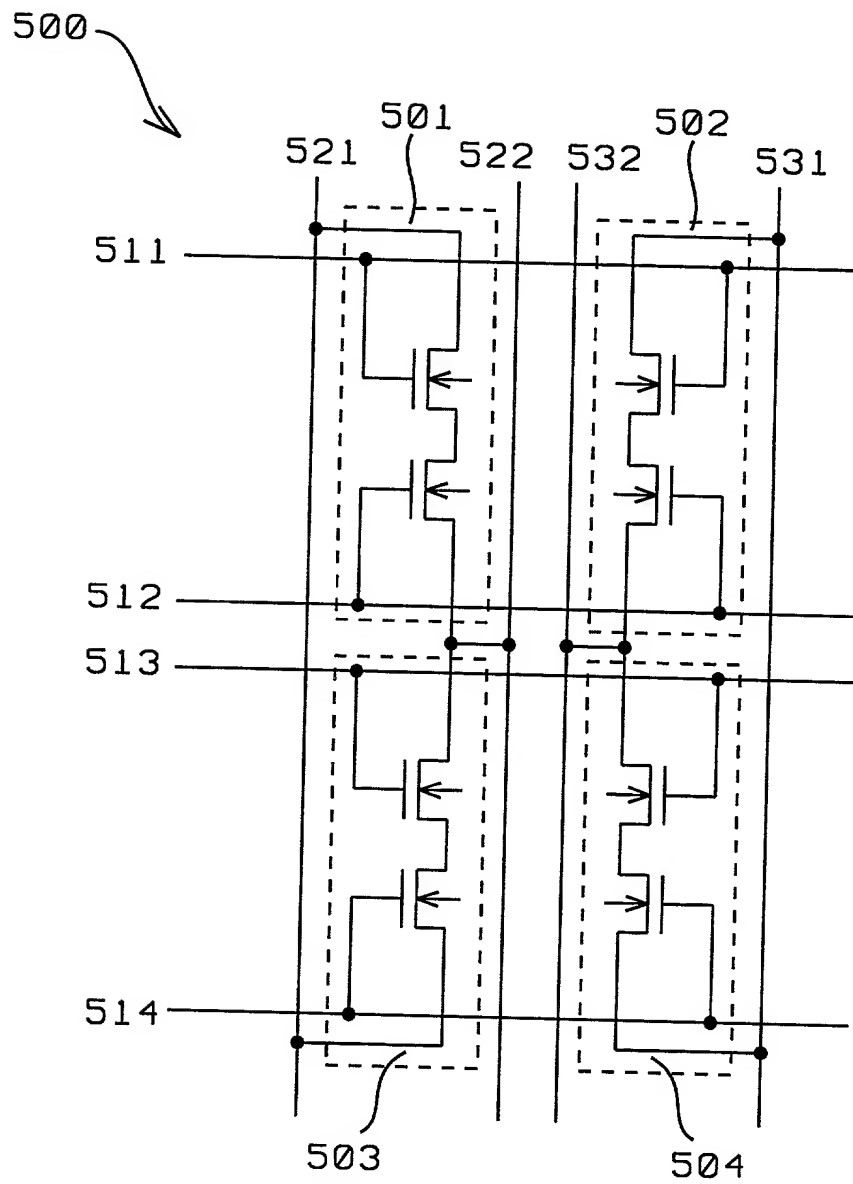


FIG. 6

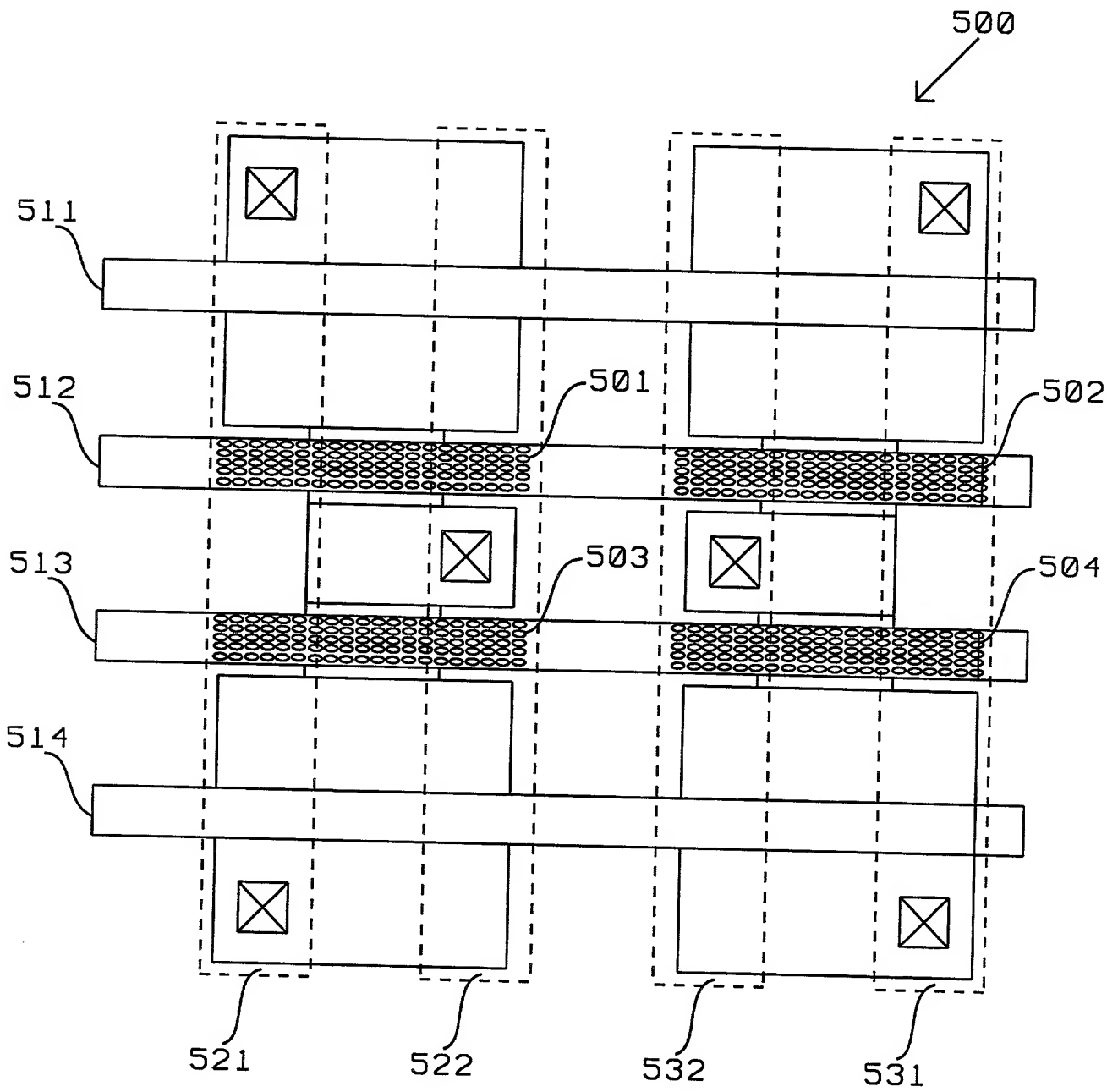


FIG. 7

MODE	Selected WL		Non-Selected WL			
	Vcg	Vag	Vcg	Vag	Vs	Vd
ERASE	-10 VOLTS	0 VOLT	-	-	5 VOLTS	HIGH Z
PROGRAM	Vpgm	Vcc	-2.5 VOLTS	Vcc	HIGH Z	-5 VOLTS
PROGRAM INHIBIT	Vpgm	Vcc	-2.5 VOLTS	Vcc	HIGH Z	-5 VOLTS
READ	Vcc	Vcc	0 VOLTS	0 VOLTS	0 VOLTS	1 VOLT
						0 VOLTS

FIG. 8a

MODE	Selected WL		Non-Selected WL			
	Vcg	Vag	Vcg	Vag	Vs	Vd
ERASE	-15 VOLTS	0 VOLT	-	-	0 VOLTS	HIGH Z
PROGRAM	Vpgm	8 VOLTS	2.5 VOLTS	8 VOLTS	HIGH Z	0 VOLTS
PROGRAM INHIBIT	Vpgm	8 VOLTS	2.5 VOLTS	8 VOLTS	HIGH Z	5 VOLTS
READ	Vcc	Vcc	0 VOLTS	0 VOLTS	0 VOLTS	1 VOLT
						0 VOLTS

FIG. 8b